

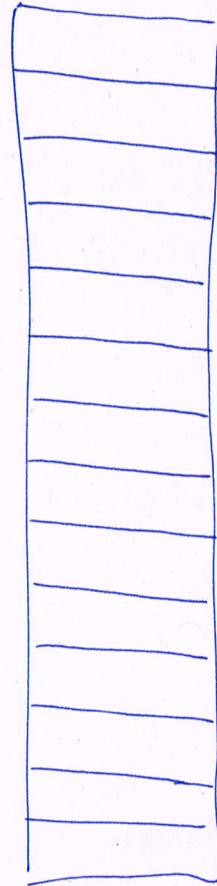


CPU

 = 0 = frei

 = 1 = locked

RAM



lock

Listing 5.1: TSL-Implementierung mit xchg.

```
1 section .text
2 enter_region: mov al, 1
3               xchg al, [lock]
4               cmp al, 0
5               jne enter_region
6               ret
7
8 leave_region: mov [lock], 0
9               ret
10
11 section .data
12 lock:        db 0
```